Declaration for Patent Application

Docket Number: 1875.4170000/JTH/GSB

As a below named inventor, I hereby declare that:

My residence, mailing address and citizenship are as stated below next to my name.

I believe I am an original, first and joint inventor of the subject matter that is claimed and for which a patent is sought on the invention entitled: <u>High Voltage Power Management Unit Architecture In CMOS Process</u>,

the specification of which is a	ttached hereto unless the	e following box is checked:		
		International Application Number <u>to be assi</u> applicable).	gned; and	
I hereby state that I have revie amended by any amendment r		contents of the above identified specification	ı, including the cl	laims, as
continuation-in-part application	ons, material information	material to patentability as defined in 37 C.F. that became available between the filing date continuation-in-part application.		
patent, inventor's or plant bre at least one country other than	eder's rights certificate(s the United States of An or patent, inventor's or p	C. § 119(a)-(d) or (f), or § 365(b) of any foreis, or § 365(a) of any PCT international applinerica, listed below and have also identified blant breeder's rights certificate(s), or PCT interhich priority is claimed.	cation, which des below, by checking	signated
Prior Foreign Application(s)			Priority	Claimed
(Application No.)	(Country)	(Day/Month/Year Filed)	□ Yes	□ No
(Application No.)	(Country)	(Day/Month/Year Filed)	□ Yes	□ No
				*
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Appl. No.: To Be Assigned Docket No.: 1875.4170000/JTH/GSB

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under 18 U.S.C. § 1001 and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

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